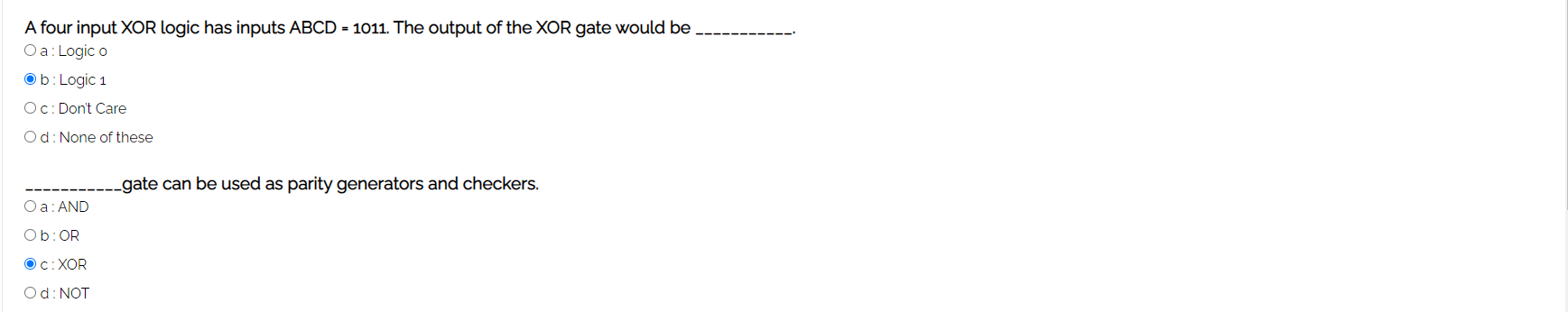
|  |  |
| --- | --- |
| **Name :-** | Aryan Dilipbhai Langhanoja |
| **Enroll No: -** | 92200133030 |

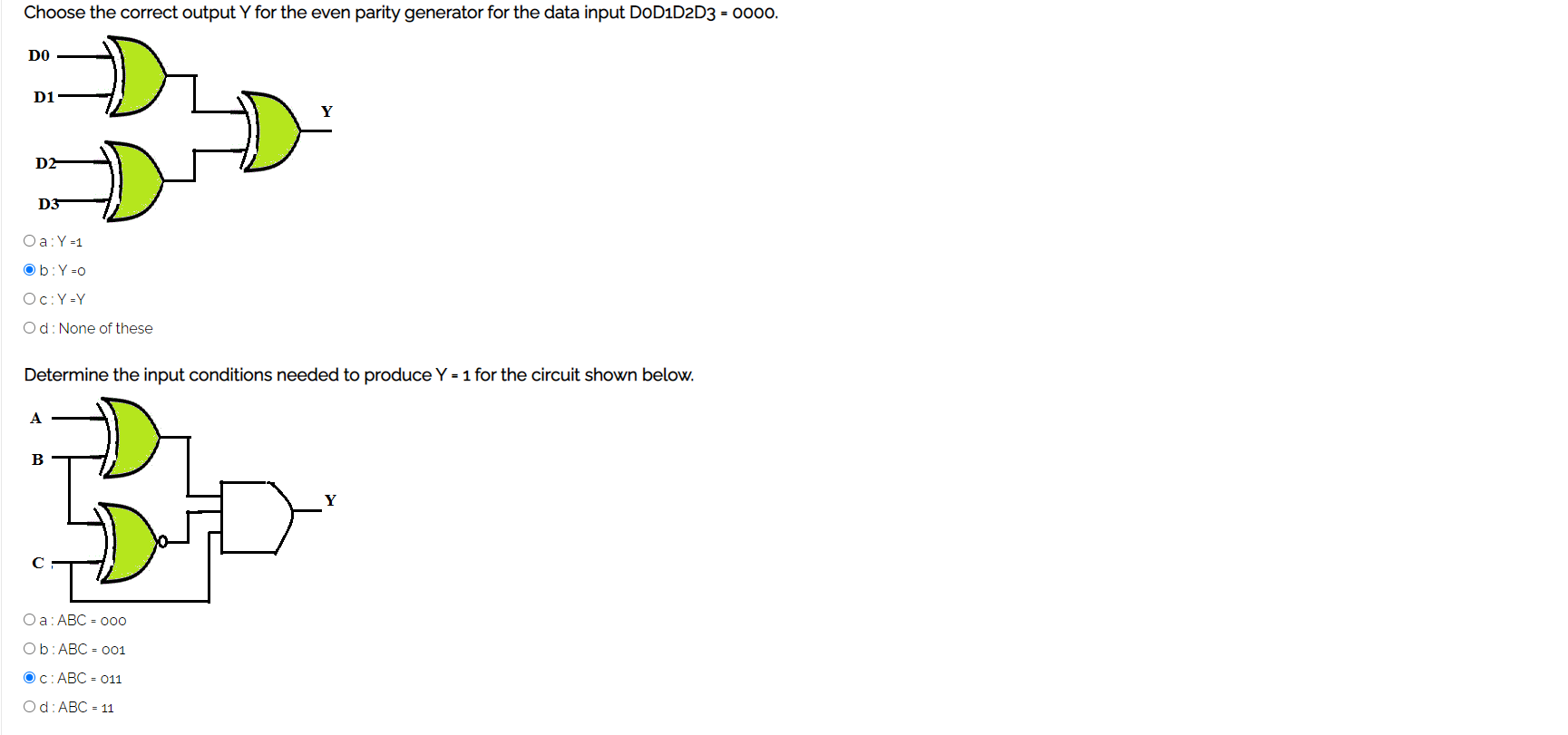
## XOR gate and its application in staircase light control

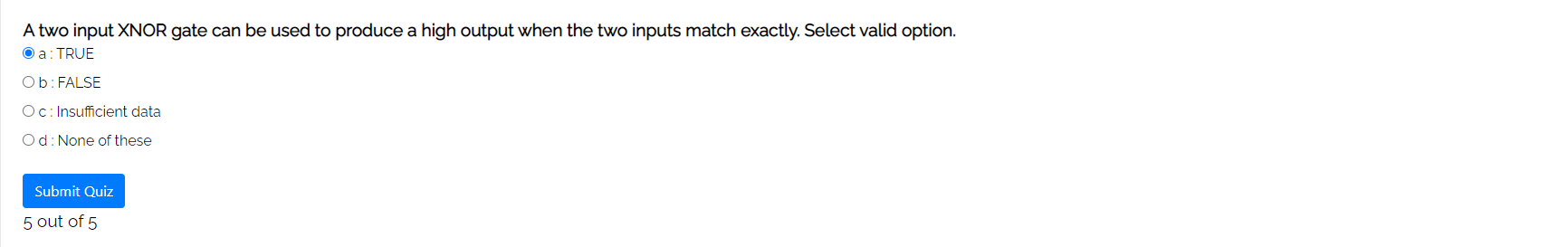
* **Aim** :-

To build automobile alarm application using basic logic gates on a generalized simulator canvas and verify its truth table.

* **Pre-test :-**

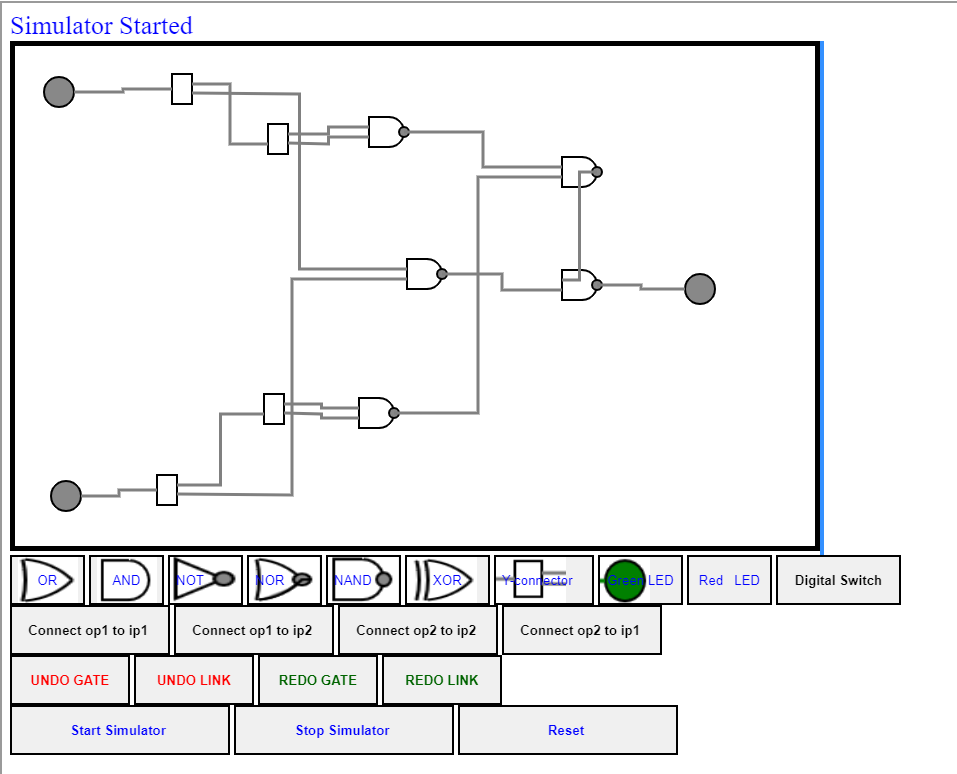
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* **Simulation :-**

**Task:-** Construct and verify XOR gate logic using only minimum of 4 NAND gates.

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* **Post-test :-**

